

# Parallelized Common Factor Attack on RSA

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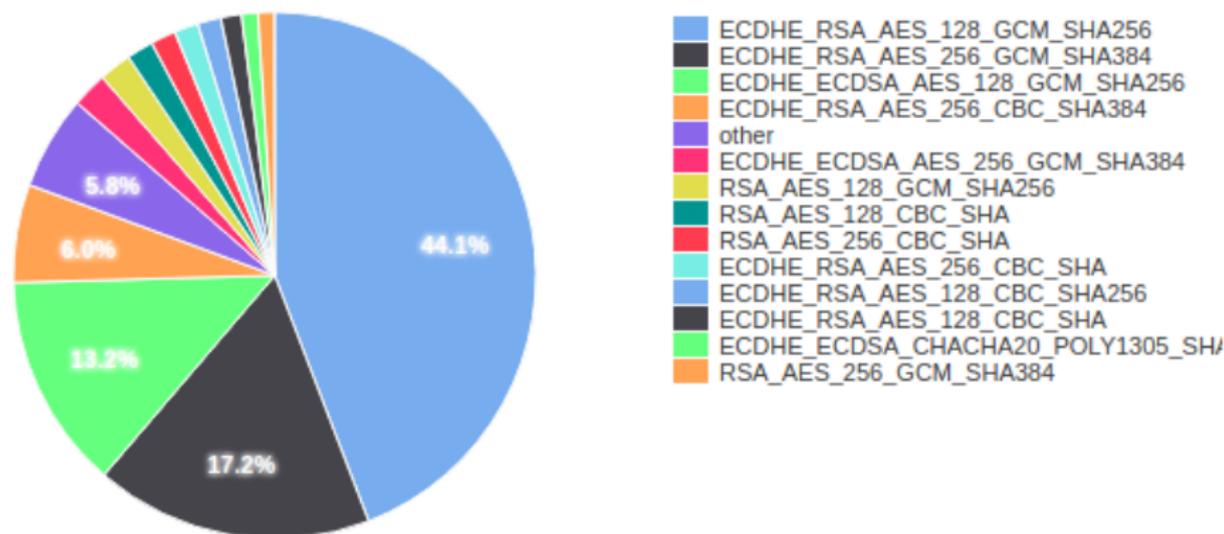
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## RSA — Most widely used Public Key Cipher

RSA Signature is used in more than 80% SSL ciphersuites in practice.

Source of data (SSL for the last 30 days) : <https://notary.icsi.berkeley.edu/>



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However, this assumption is violated if RSA moduli share a common factor:

$$N_1 = pq_1, N_2 = pq_2 \Rightarrow$$

$$\gcd(N_1, N_2) = p$$

### *Intuitive Assumption*

If two 512-bit RSA primes  $p$  and  $q$  are chosen uniformly at random, then the chance of getting the same prime twice is approx.  $2^{-256}$  (birthday collision).

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## *Counter-Intuitive Reality*

In 2012, Heninger et al. and Lenstra et al. independently discovered that around **0.75%** of TLS certificates across the Internet shared RSA primes.

Heninger et al. also conjectured that another **1.70%** may be susceptible.

In 2013, Bernstein et al. demonstrated similar vulnerabilities in RSA moduli embedded in smart cards of Taiwan's national "Citizen Digital Certificate".

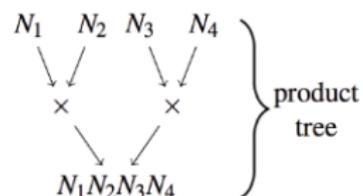
Heninger et al., USENIX Security Symposium, 2012

Lenstra et al., IACR Cryptology ePrint Archive, 2012

Bernstein et al., ASIACRYPT 2013

## Step 1 : Product Tree

$$P = \prod N_i$$



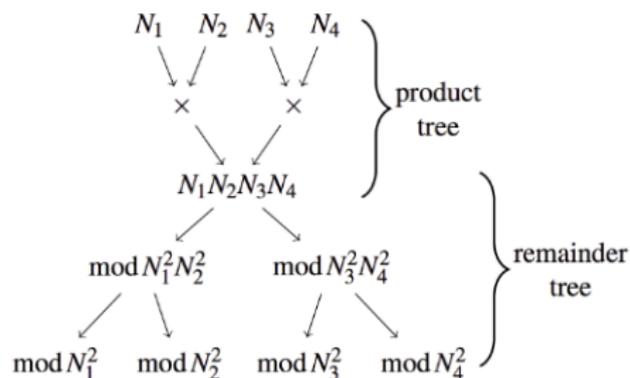
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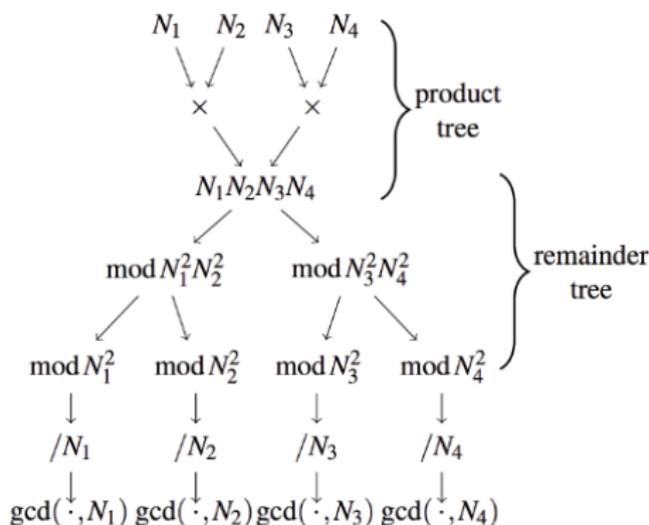
**Step 2 :** Remainder Tree

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**Step 3 :** Compute

$$\gcd(N_i, z_i/N_i)$$

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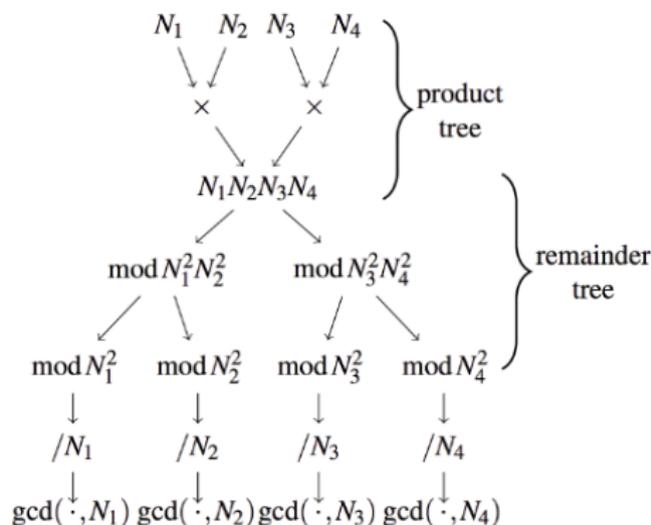
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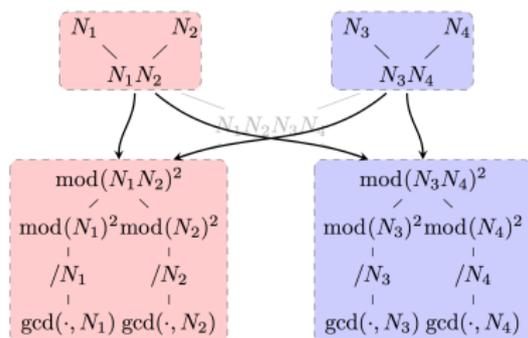


Complexity  $\sim O(mn(\log n)^2 \log \log n)$

32 GB of memory and around 60 to 70 GB of storage for scratch calculations

# Common Factor Attack using Parallel Batch-GCD

In 2016, Hastings et al. proposed a parallel version of batch-GCD algorithm.



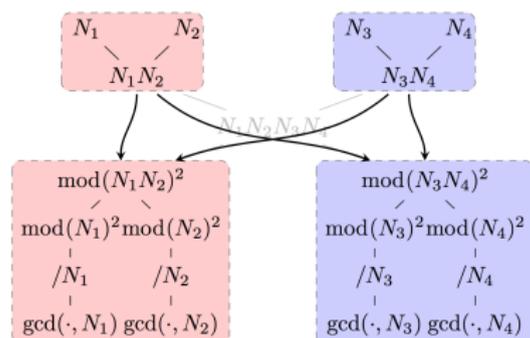
The RSA moduli dataset is partitioned into subsets and the product tree for

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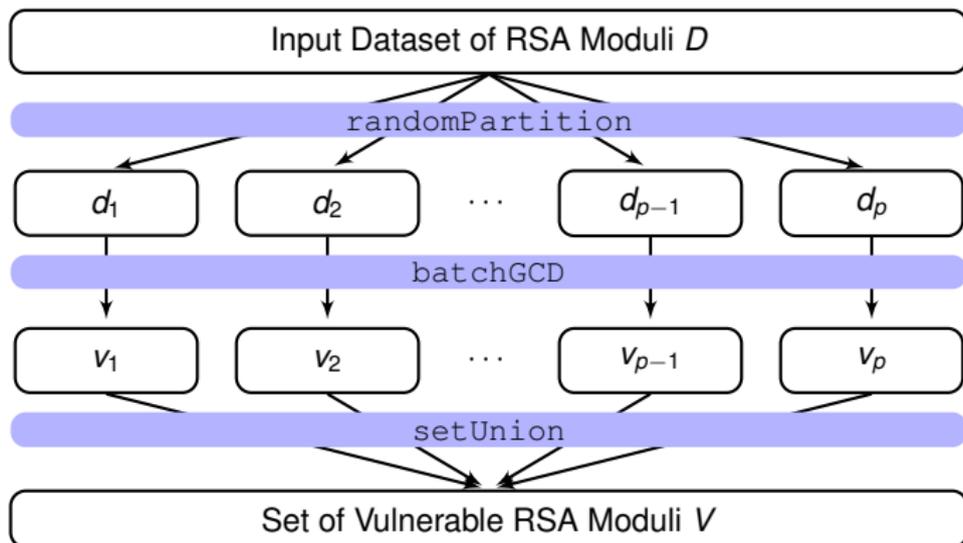
is constructed independently for each subset, making this stage **parallel**.

But, the remainder tree is still constructed considering all subset products.

Hastings et al., Internet Measurement Conference, 2016

## Our Idea — Parallellized Common Factor Attack

*Our contribution* — We propose a completely parallel version of batch-GCD algorithm to achieve similar results in a resource constrained environment.



**Figure :** One complete iteration of our proposed Parallellized Batch-GCD

## Our Idea — Parallellized Common Factor Attack

*Primary concern* — How many iterations are enough to recover all the factors?

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### Theorem (Optimal number of Iterations)

*Suppose there exist  $X$  vulnerable RSA moduli in input dataset  $D$ . Then our algorithm recovers an expected number of  $\epsilon X$  vulnerable moduli if we set*

$$k \approx \frac{\log(1 - \epsilon)}{\log m + \log(p - 1) - \log(mp - 1)},$$

*where  $\epsilon$  is a user-defined accuracy parameter,  $m$  is the user-defined constraint of the individual computing nodes, and  $p \sim |D|/m$  is the number of partitions.*

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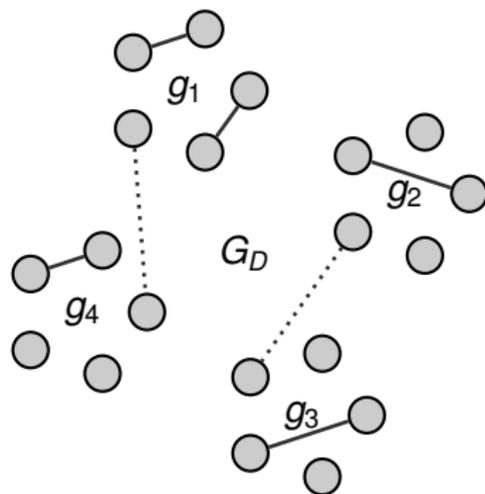
*where  $\epsilon$  is a user-defined accuracy parameter,  $m$  is the user-defined constraint of the individual computing nodes, and  $p \sim |D|/m$  is the number of partitions.*

One may interpret  $k$  given  $D$  and  $\epsilon$  as :  $k \approx \frac{\log(1 - \epsilon)}{\log(|D| - |D|/p) - \log(|D| - 1)}$

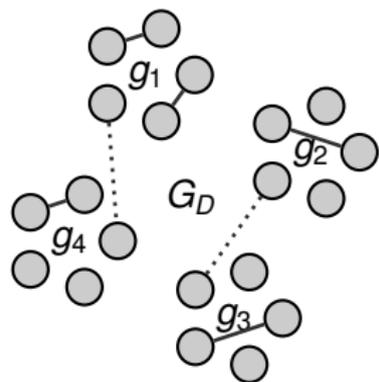
## Proof of Theorem — Optimal number of Iterations

Consider the complete dataset of RSA moduli as an induced graph  $G_D$ , where the RSA moduli  $N_i$  are vertices and an edge  $e_{(N_i, N_j)}$  exists iff  $\gcd(N_i, N_j) > 1$ .

Partitioning the RSA moduli dataset is identical to partitioning graph  $G_D$ , and thus, our algorithm discovers edges within subgraphs, and misses the others.



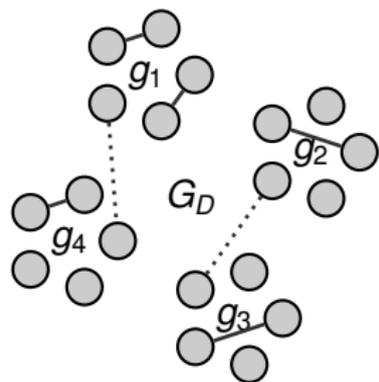
## Proof of Theorem — Optimal number of Iterations



The probability that we will miss a specific edge  $e_{(N_i, N_j)}$  in  $G_D$  after one execution of our algorithm:

$$\begin{aligned} P_{i=1} &= 1 - \frac{\text{total number of edges in } \{g_1, g_2, \dots, g_p\}}{\text{total number of edges in } G_D} \\ &\approx 1 - \frac{\text{edges in complete supergraph of } \{g_1, g_2, \dots, g_p\}}{\text{edges in complete supergraph of } G_D} \\ &\approx 1 - \frac{p \times \binom{m}{2}}{\binom{mp}{2}} = 1 - \frac{m-1}{mp-1} = \frac{m(p-1)}{mp-1} \end{aligned}$$

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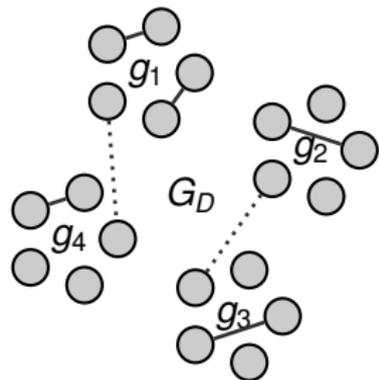
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The fraction of edges recovered after  $k$  iterations is  $\epsilon \approx 1 - \left( \frac{|D| - |D|/p}{|D| - 1} \right)^k$

**Input** : Set of moduli  $D$ , constraint  $m$ , accuracy  $\epsilon$

**Output**:  $V$  — set of vulnerable moduli in  $D$

```
1  $p \leftarrow \text{ceiling}(|D|/m)$  ;
2  $k \leftarrow \text{chooseIteration}(m, p, \epsilon)$  ;
3 for  $i \leftarrow 1$  to  $k$  do
4    $\{d_1, d_2, \dots, d_p\} \leftarrow \text{randomPartition}(D, p)$  ;
5    $\{v_1, v_2, \dots, v_p\} \leftarrow \text{batchGCD}(\{d_1, d_2, \dots, d_p\})$  ;
6    $V_i \leftarrow \text{setUnion}(\{v_1, v_2, \dots, v_p\})$  ;
7 end
8  $V \leftarrow \text{setUnion}(\{V_1, V_2, \dots, V_k\})$  ;
```

## Our Algorithm — Parallelized Batch-GCD

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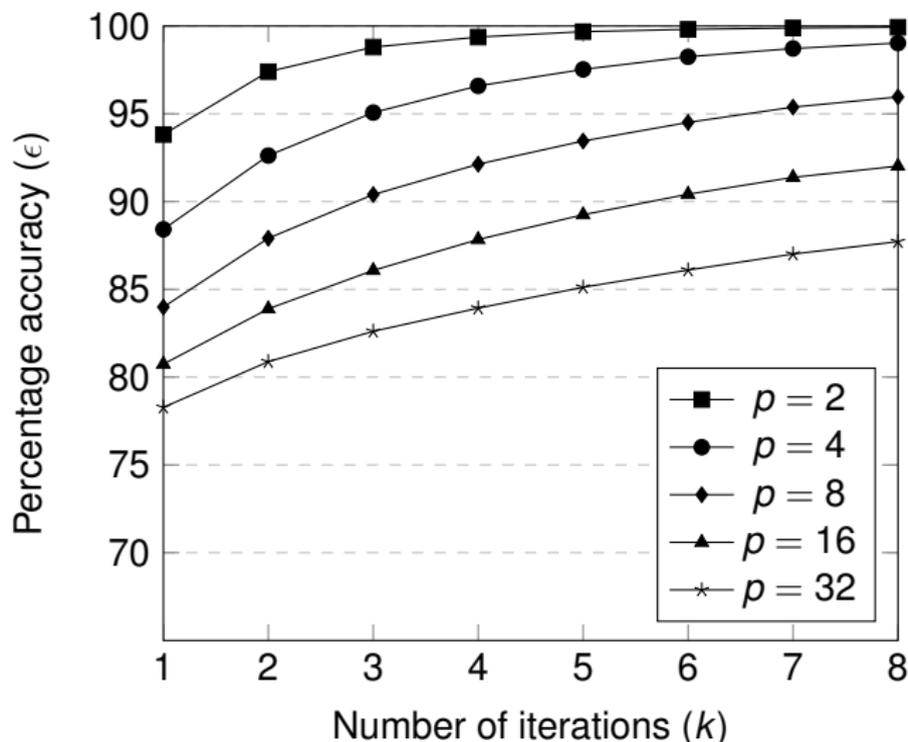
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Line 2 :  $k$  given  $D$  and  $\epsilon$  is chosen as  $k \approx \frac{\log(1 - \epsilon)}{\log(|D| - |D|/p) - \log(|D| - 1)}$

The algorithm recovers  $\epsilon$  fraction of vulnerable RSA moduli from the dataset.

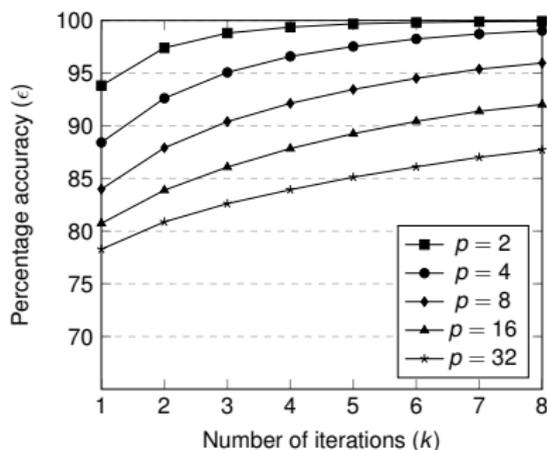
## Our Algorithm — Practical Performance Results

Checked  $\epsilon$  for various choices of  $p = 2, 4, 8, 16, 32$ , and  $k = 1, 2, 3, \dots, 9$



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In practice, with Intel Core i5 4210U CPU, 4 GB RAM

$p = 8$  partitions and  $k = 3$  iterations resulted in  $> 90\%$  recovery

$p = 32$  partitions and  $k = 5$  iterations resulted in  $> 85\%$  recovery

## Scope — Potential extensions of Our Proposal

Extend our algorithm to include the partially parallel tree of Hastings et al.

Extend our proposal to include the more sophisticated approaches of finding vulnerable RSA moduli, using Coppersmith-type lattice based attacks, as done by Bernstein et al. on the Taiwan's national "Citizen Digital Certificate".



Lenstra, A.K., Hughes, J.P., Augier, M., Bos, J.W., Kleinjung, T., Wachter, C.:

**Ron was wrong, Whit is right.**

IACR Cryptology ePrint Archive **2012** (2012) 64



Heninger, N., Durumeric, Z., Wustrow, E., Halderman, J.A.:

**Mining your ps and qs: Detection of widespread weak keys in network devices.**

In: Proceedings of the 21th USENIX Security Symposium, Bellevue, WA, USA, August 8-10, 2012. (2012) 205–220



Hastings, M., Fried, J., Heninger, N.:

**Weak keys remain widespread in network devices.**

In: Proceedings of the 2016 ACM on Internet Measurement Conference, IMC 2016, Santa Monica, CA, USA, November 14-16, 2016. (2016) 49–63



Bernstein, D.J., Chang, Y., Cheng, C., Chou, L., Heninger, N., Lange, T., van Someren, N.:

**Factoring RSA keys from certified smart cards: Coppersmith in the wild.**

In: Advances in Cryptology - ASIACRYPT 2013 - 19th International Conference on the Theory and Application of Cryptology and Information Security, Bengaluru, India, December 1-5, 2013, Proceedings, Part II. (2013) 341–360

# Thank You!

A CRYPTO NERD'S  
IMAGINATION:

HIS LAPTOP'S ENCRYPTED.  
LET'S BUILD A MILLION-DOLLAR  
CLUSTER TO CRACK IT.



WHAT WOULD  
ACTUALLY HAPPEN:

HIS LAPTOP'S ENCRYPTED.  
DRUG HIM AND HIT HIM WITH  
THIS \$5 WRENCH UNTIL  
HE TELLS US THE PASSWORD.

